

**TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT**  
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.  
584-25557-US

In Re Application Of: **Green et al.**

Serial No.  
09/746,362

Filing Date  
December 22, 2000

Examiner  
C. Das

Group Art Unit  
2122

Title: **METHOD AND SYSTEM AND ARTICLE OF MANUFACTURE FOR AN N-TIER SOFTWARE COMPONENT ARCHITECTURE OILFIELD MODEL**



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Commissioner for Patents  
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MAY 18 2004  
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**37 CFR 1.97(b)**

1. ☐ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

**37 CFR 1.97(c)**

2. ☒ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:
- ☐ the statement specified in 37 CFR 1.97(e);
- OR**
- ☒ the fee set forth in 37 CFR 1.17(p).

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**ARCHITECTURE OILFIELD MODEL**

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**Payment of Fee**

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(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

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Dated: 5/12/04

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(Use several sheets if necessary)

**09/745,362**

Green et al.

**December 22, 2000**

2122

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## PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

SUBCLASS	Translation	
	YES	NO

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

**Fox, Otto; "Using BeanExtenders Dipping Technique"; Component Technology, IBM Software Solutions Division; March 1998, Pages 1-8**

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**U.S. Patent Application Serial Number 09/746,155 entitled METHOD AND SYSTEM FOR DESIGNING AN N-TIER ARCHITECTURE FOR USE IN GENERATING SOFTWARE COMPONENTS filed December 22, 2000**

DATE CONSIDERED \_\_\_\_\_

**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.